

NODE INTERCONNECTIONS AND CONTROL

Fig 1

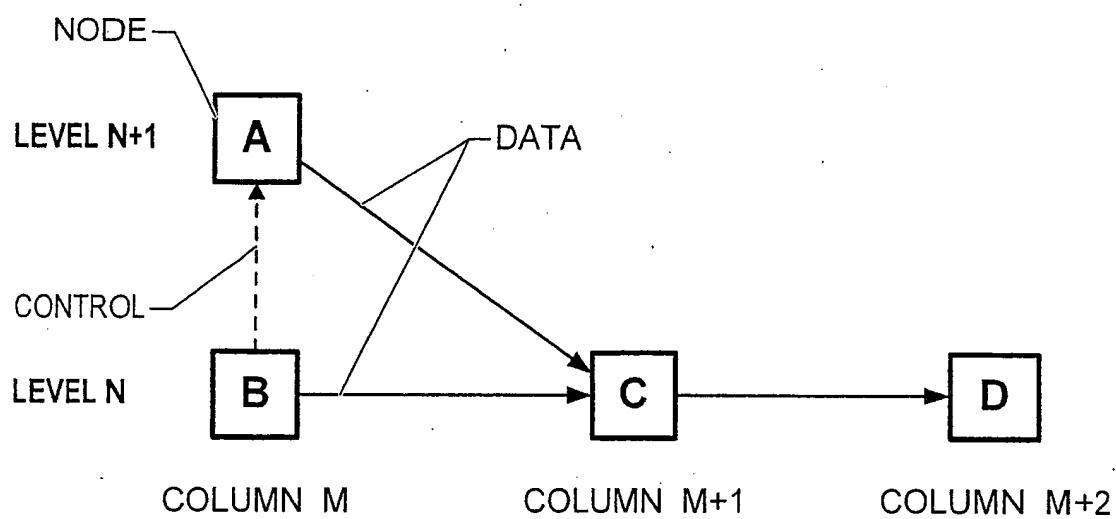


Fig 2

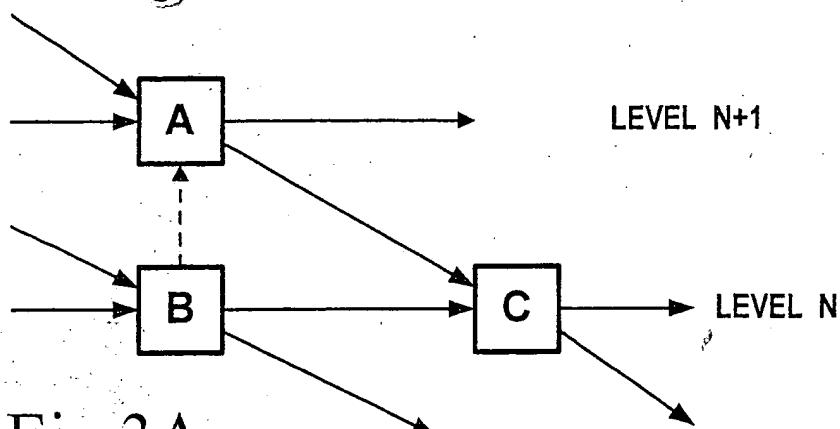


Fig 3A

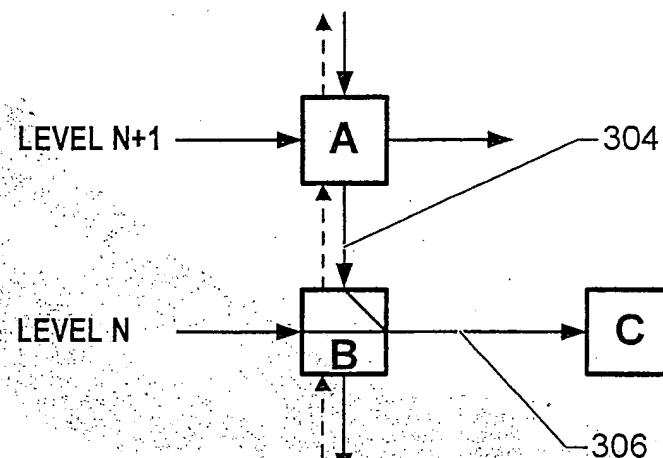
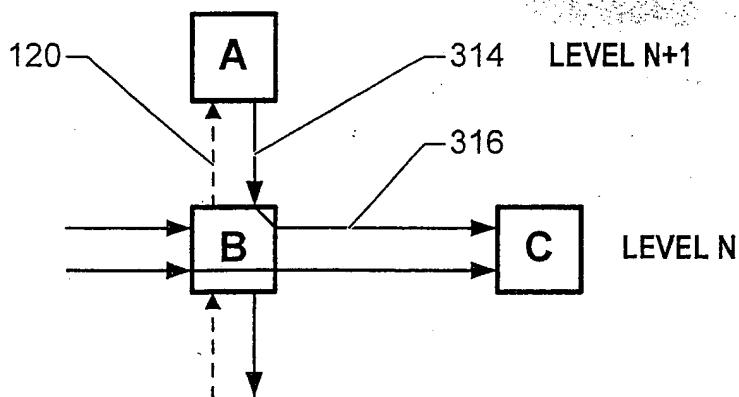
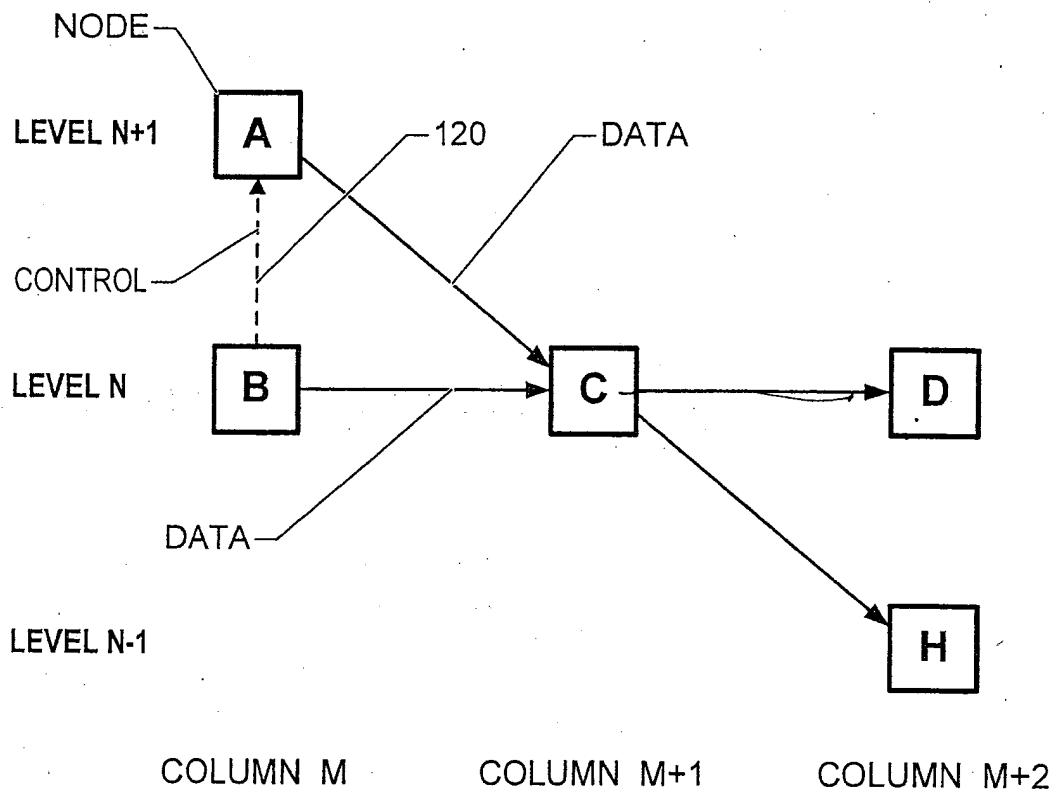


Fig 3B



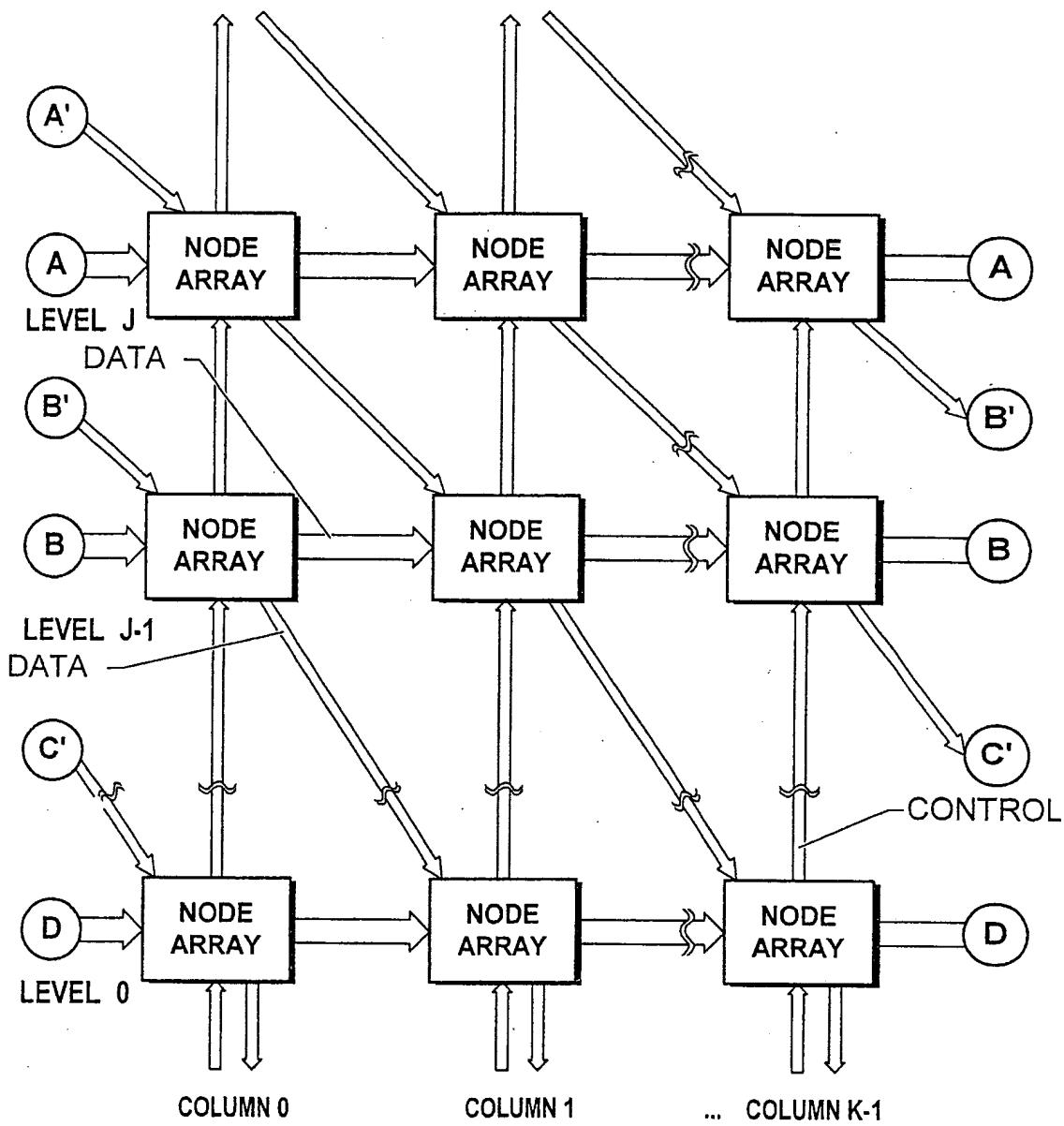
ALTERNATE NODE INTERCONNECTIONS

Fig 3C



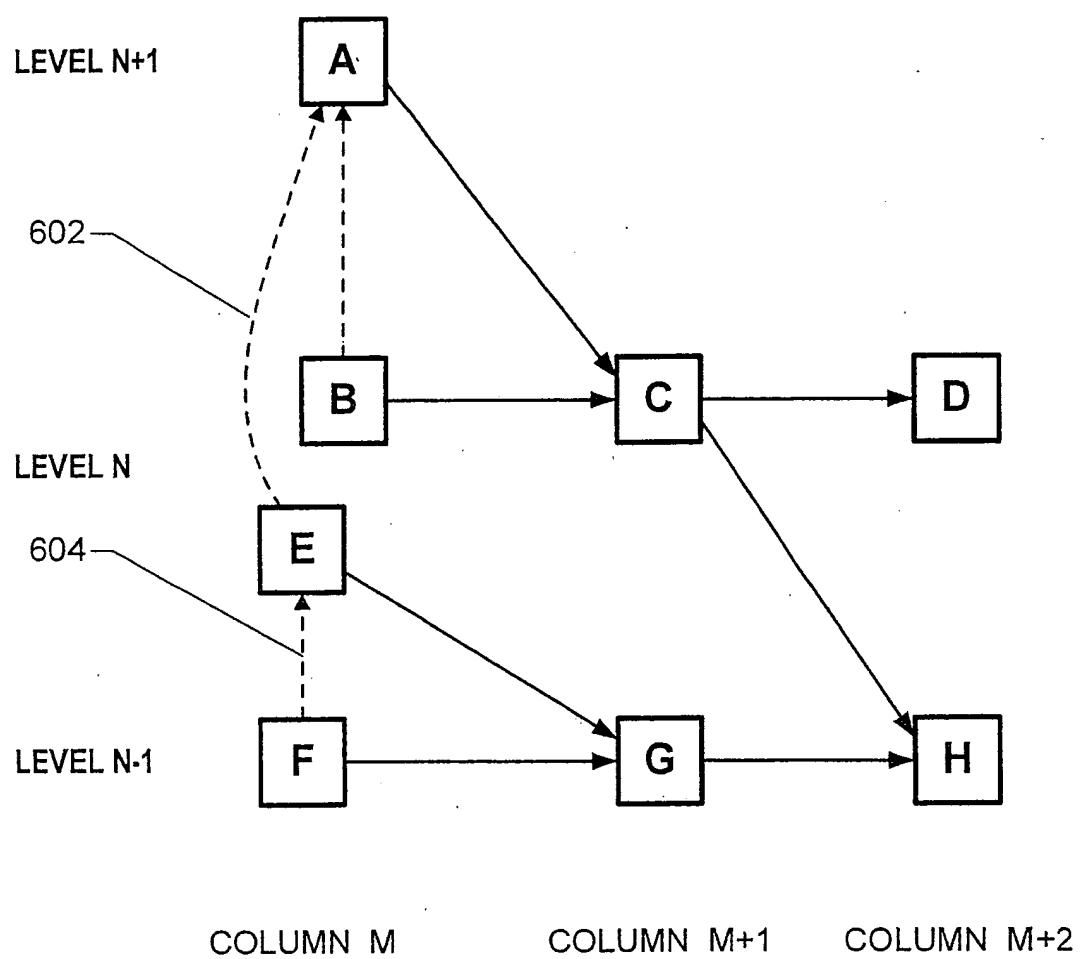
NODES ON THREE LEVELS

Fig 4



INTERCONNECT BLOCK DIAGRAM

Fig 5



INTERLEVEL CONTROL SIGNALS

Fig 6A

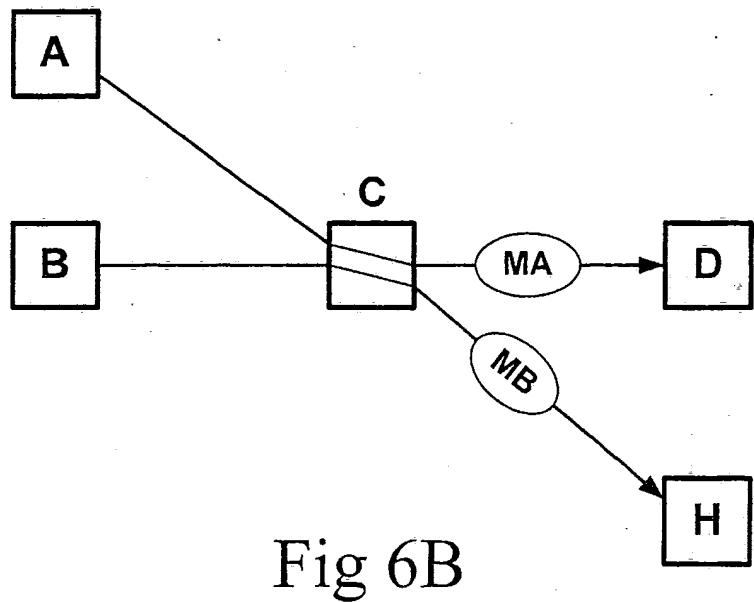


Fig 6B

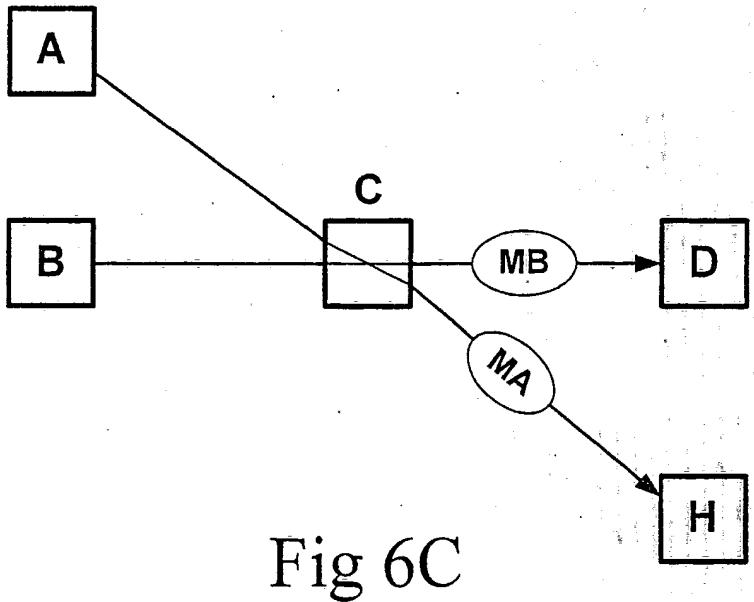


Fig 6C

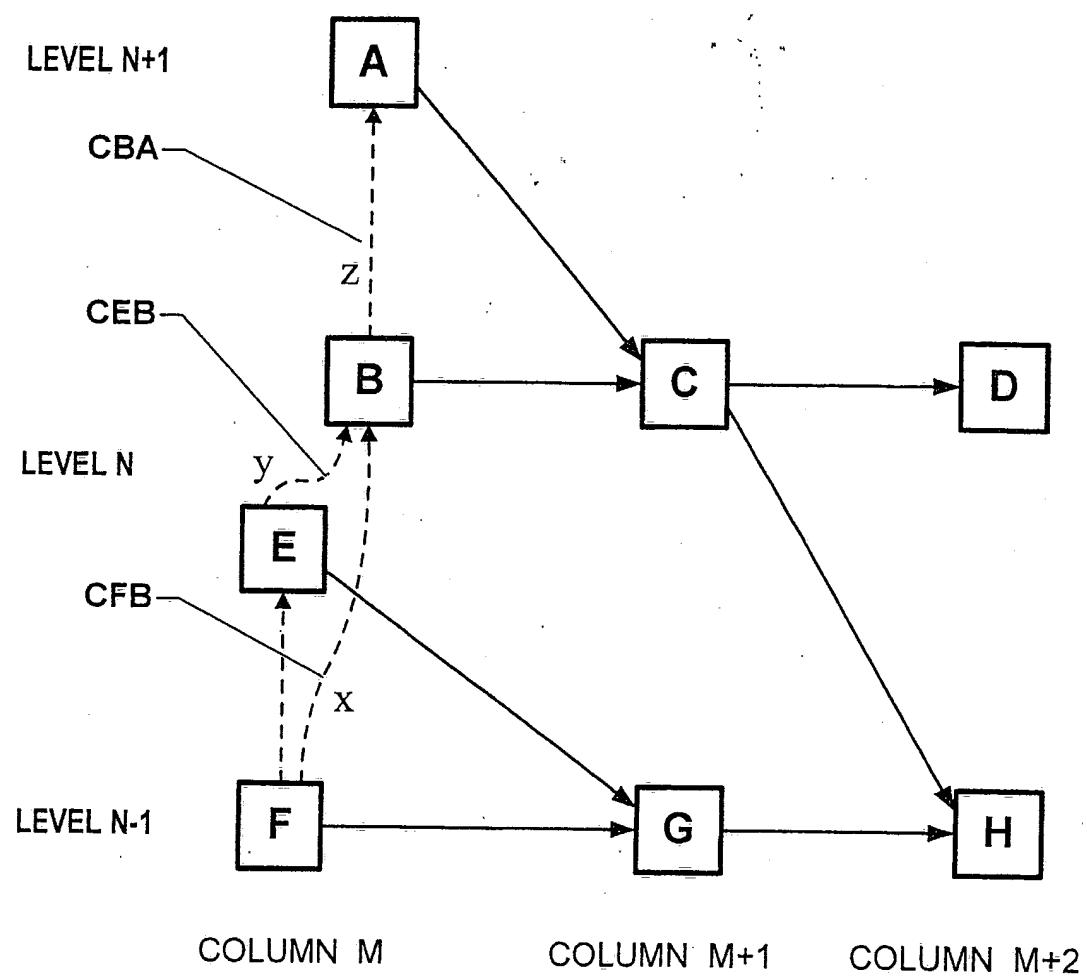


Fig 7

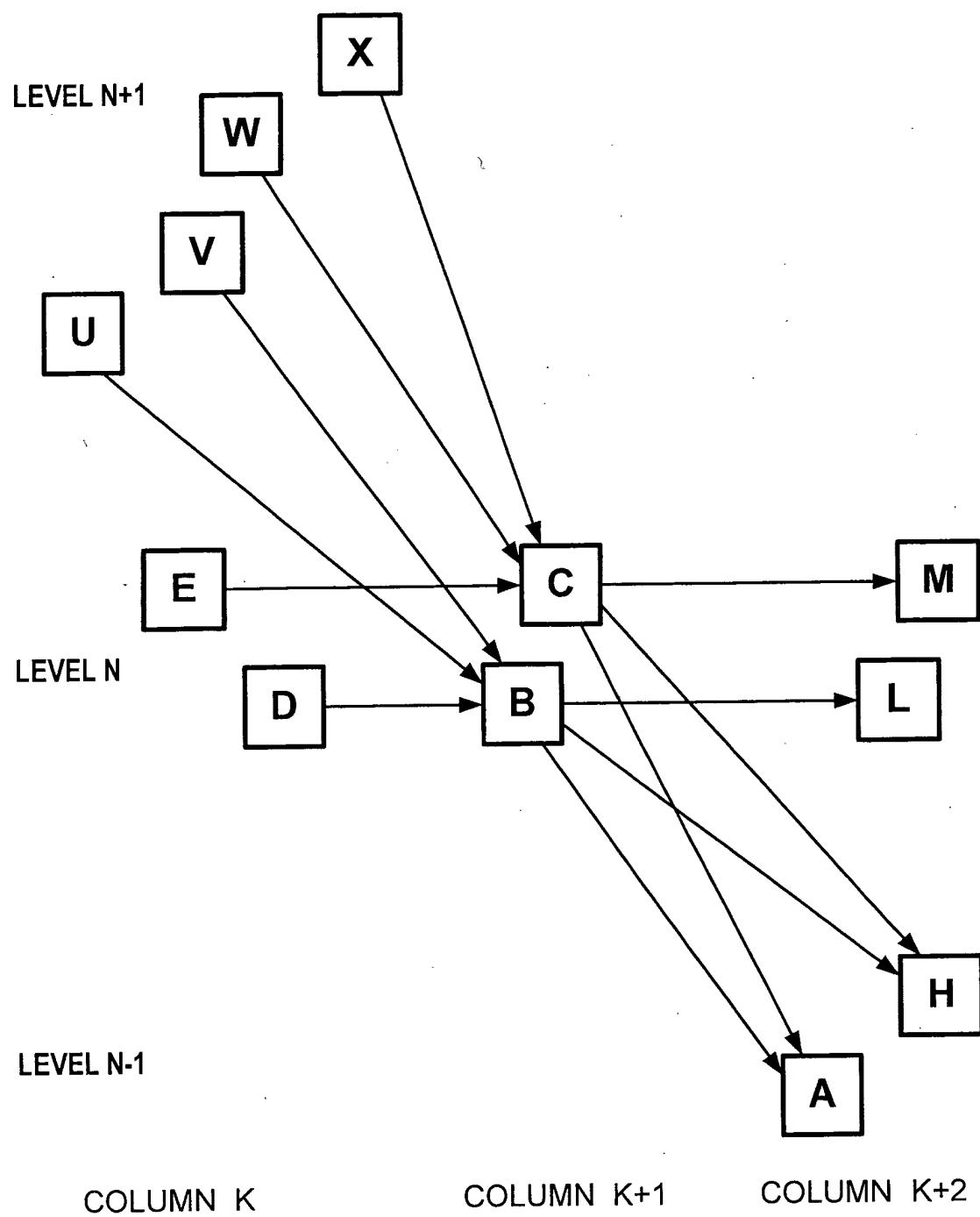


Fig 8